

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,461,304
Issue Date: December 2, 2008
Inventor(s): Eitan Rosen
Application No: 10/614,403
Filing Date: July 7, 2003
Title: INTEGRATED CIRCUIT TEST USING CLOCK SIGNAL MODIFICATION
Attorney Docket: MP0278

Commissioner for Patents
Certificate of Correction Branch
P.O. Box 1450
Alexandria, Virginia 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION - ERROR DUE TO PTO MISTAKE

Sir:

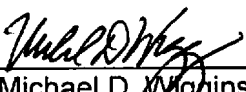
We have reviewed the above referenced patent and have found errors which appear to require a Certificate of Correction.

Attached herewith is an original copy of the Patent and Trademark Office Certificate of Correction form which we request be approved for the above-indicated patent.

There should be no charge for this Certificate of Correction since the errors were made by the PTO. However, if there are any fees that are due, please charge them to Deposit Account No. 08-0750.

Respectfully submitted,

Dated: December 23, 2008
HARNESS, DICKEY & PIERCE, P.L.C.
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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 7,461,304

APPLICATION NO: 10/614,403

DATED : December 2, 2008

INVENTOR(S) : Eitan Rosen

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7, Line 4

Delete "40Z" and insert -- 402 --

Column 7, Line 63

Delete "failing" and insert -- falling --

MAILING ADDRESS OF SENDER: Harness, Dickey & Pierce, P.L.C.
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PATENT NO. 7,461,304

No. of additional copies

1

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